a receiver for receiving said first spread spectrum signal, comprising:

a first receiver circuit for measuring total received signal power in said predetermined bandwidth;

a second receiver circuit, coupled to said first receiver circuit, for measuring a ratio of signal energy of said first received spread-spectrum signal to a total received power spectral density in said predetermined bandwidth; and

a processor, coupled to said first receiver circuit and said second receiver circuit, for calculating power of said first received spread-spectrum signal in response to said measured total received signal power and said measured ratio of signal power.

20. The communication system of claim **19** wherein said first received spread spectrum signal is a pilot signal and said first transmitter is a code division multiple access (CDMA) base station.

21. The communication system of claim **20** wherein said first receiver circuit comprises a power measurement circuit for generating a total received power signal in response to said measured total received signal power, said second receiver circuit comprises at least one demodulation element for despreading said pilot signal and for generating a CDMA energy ratio signal in response to said measured ratio of signal energy, and said processor generates a CDMA pilot power signal in response to said total received power signal and said CDMA energy ratio signal.

22. The communication system of claim **21** further comprising a display coupled to said processor for generating a quality indication in response to said CDMA pilot power signal.

23. The communication system of claim **19** wherein said first transmitter transmits a second spread spectrum signal and said receiver receives said second spread spectrum signal and said receiver further comprises a decoder, coupled to said second receiver circuit and said processor, for extracting a transmitted power level signal from said second received spread spectrum signal.

24. The communication system of claim **23** wherein said first received spread spectrum signal is a pilot signal, said second received spread spectrum signal is a sync signal, and said first transmitter is a code division multiple access (CDMA) base station.

25. The communication system of claim **24** wherein said first receiver circuit comprises a power measurement circuit for generating a total received power signal in response to said measured total received signal power, said second receiver circuit comprises at least one demodulation element for despreading said pilot signal and for generating a CDMA energy ratio signal in response to said measured ratio of signal energy, and said processor generates a path loss signal in response to said total received power signal, said CDMA energy ratio signal, and said transmitted power level signal.

26. The communication system of claim **25** further comprising a second transmitter coupled to said processor, said second transmitter having a variable gain and adjusting said variable gain in response to said path loss signal.

27. The communication system of claim **26** further comprising a display coupled to said processor for generating a quality indication in response to said path loss signal.

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